



PHYSICS

BOOKS - PUNJAB BOARD PREVIOUS YEAR PAPERS

Digital Electronics

Exercise

1. Write down the truth table of AND gate and also draw its logic symbol.



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2. Write down the truth table of OR gate and also draw the logic symbol.



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3. What is logic gate ?



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4. Draw logic symbol of AND gate.



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5. Draw the logic symbol of NOR gate.



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6. Draw the logic symbol of NAND gate.



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7. Draw the logic symbol of NAND gate.



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8. What is Boolean expression?



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9. What is logic gate ?



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10. The output of OR gate is 1

A. if either input is zero

B. if both inputs are zero

C. only, if both inputs are 1

D. if either or both inputs are 1.

Answer:



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11. Write the truth table of AND gate.



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12. Write the truth table of OR gate.



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13. Give truth table on NAND gate.



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14. Draw the logic symbol and write truth table of NAND logic gate



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15. Draw the logic symbol and write truth table of NOR logic gate



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16. Draw the logic symbol and write truth table of NOR logic gate



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17. How 'NAND'logic gate is obtained from basic gates ? Draw its logic symbol and write its truth table.



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18. Write Boolean expression and the truth table of OR



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19. Write Boolean expression and the truth table of NAND



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20. Write Boolean expression and the truth table of NOR gate



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21. What is logic symbol and truth table for NAND gate ? Also give its Boolean expression.



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22. Give the logic symbol, truth table and Boolean expression for OR gate.



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23. Give the logic symbol, truth table and Boolean expression for NAND gate.



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24. Give the logic symbol, truth table and Boolean expression for NOR gate.



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25. Give the logic symbol, truth table and Boolean expression for AND gate.



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26. Draw logic symbol of AND gate.



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27. Write logic symbol, Boolean expression and truth table of AND and NOR gate.



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28. Write logic symbol, Boolean expression and truth table of NAND and OR gate.



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29. Write logic symbol, Boolean expression and truth table of NOT and OR gate.



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30. Write the truth table of NAND gate. Draw a symbol of NAND gate. Draw a circuit to obtain AND gate using NAND gate only.



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31. Write the truth table of NAND gate. Draw its symbol. Draw a circuit to obtain NOT gate using NAND gate only.



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32. Write the truth table of NOR gate. Draw the symbol of NOR gate. Draw a circuit to obtain OR gate using NOR gate only.



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33. Give the logic symbol, truth table and Boolean expression for AND gate.



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34. What is NOT gate ? Give its symbol, Boolean expression and truth table.



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35. Write Boolean expression and the truth table of OR



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36. Draw the logic symbol of AND gate. Write its truth table also. How can one realise such a gate in actual practice by using junction diodes ?



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37. Draw the logic symbol of NOT gate. Write its truth table. Explain, how this gate can be realized in actual practice by using transistor



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38. Draw the logic symbol of AND gate. Write its truth table also. How can one realise such a gate in actual practice by using junction diodes ?



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39. Draw the logic symbol of OR gate. Write its truth table. Explain how this gate can be realized in actual practice by using junction diodes.



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