



## PHYSICS

## **BOOKS - BITSAT GUIDE**

## SEMICONDUCTOR DEVICES AND LOGIC GATES

**Practice Exercise** 

- 1. If the resistivity of copper is  $1.7 imes 10^{-6} \Omega cm$
- , then the mobility of electrons in copper, if

each atom of copper contributes one free electron for conduction, is [The amomic weight of copper is 63.54 and its density is 8.96g/cc]:

A. 23.36  $cm^2$ /Vs

B. 503.03  $cm^2$ /Vs

C. 43.25  $cm^2$ /Vs

D. 88  $cm^2/Vs$ 

Answer: C

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2. The conductivity of a semiconductor increases with increase in temperature because

A. Number density of free current carries increases

B. relaxation time increases

C. both number density of carriers and

relaxation time increases

D. number density of current carriers increases, relaxation time decreases but effect of decreases in relaxation time is much less than increase in number density.

Answer: D

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**3.** In a *n*-type semiconductor, which of the following statement is true?

A. Electrons are majority charge carriers and triavalent atoms are the dopants. B. Electrons are minority charge carriers and pentavaent atoms are the dopants. C. Holes are minority charge carriers and pentavalent atoms are the dopants

D. Holes are majority carriers and trivalent

atoms are the dopants.

Answer: C

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**4.** Application of a forward biase to a p-n

junction:

A. Increases the number of donors on the

n-side

B. Increase the electric field in the

depletioin zone

C. Increase the potential difference across

the depletion zones.

D. widens the depletion zone

Answer: A

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5. In a three phase full-wave rectifier of 50Hz,

the ripple frequency is

A. 300 Hz

B. 50 Hz

C. 400 Hz

D. 600 Hz

Answer: A

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**6.** What is the dimensional formula of  $\sqrt{LC}$  ?

A.  $(s)^2$ 

B. volt-s/amp

C. amp-s/volt

D. s

**Answer: D** 



7. Given, eta=49 and  $l_e$ = 2ma,  $l_c$  for a BJT is

A. 2 mA

- B. 1.96 mA
- C. 98 mA

D. 
$$\frac{2}{49}$$
 mA

#### Answer: B



8. A single ended class A transformer coupled amplifier delivers a power of 50 mW, [Math Processing Error] = 10 volts. Assuming ideal

conditions, the reflected primary resistance is

given by

A.  $2000\Omega$ 

 $\mathsf{B.}\,1000\Omega$ 

 $\mathsf{C.}\,500\Omega$ 

D.  $200\Omega$ 

Answer: B



**9.** In a vaccume diode, when the plate voltage is 40 V, the plate current is 10 mA. For a plate voltage of 160 V, the plate current is

A. 20 mA

B. 40 mA

C. 80 mA

D. 160 mA

Answer: C

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**10.** A triode is operating at  $E_c = -4V$ ,  $E_b = 175$  V and  $l_b=10$  mA. Around this operating point, the plate characteristics are linear and the parameter values are  $\mu=20$ ,  $g_m=2.5$  mA/V. If  $E_c$  is changed to -6V with  $E_b = 175$  V, the value of  $l_b$  is given by

A. 10 mA

B. 20 mA

C. 15 mA

D. 5 mA

#### Answer: D



**11.** Given the following set of values for a triode, the value of  $g_m$  and  $\mu$  are given below in the table.



A. 1.25 mA/V nad 8

B. 0.8 mA/V and 8

C. 1.25 mA/V and 10

D. 8 mA/V and 10

#### Answer: C

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### **12.** The table values of $E_b$ , $E_c$ , $I_b$ for a triode



The value of  $r_p$  in  ${\rm k}\Omega$  and  $g_m$  in mA/V are given

by

A. 50 and 0.1

B. 10 and 3.0

C. 5 and 1.5

D. 3.33 AND 1.5

#### Answer: D

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**13.** A Triode is operating at  $E_c$  = -4V,  $E_b$  = 175 V and  $l_b$ =10mA. Around this operating point, the plate characteristics are linear and the parameters are  $\mu$ =20,  $r_p$  = 8 $\Omega$ . E is changed top -6V. To obtain the same plate current the

value of  $E_b$  should be

A. 40

B. 135

C. 191

D. 215

Answer: D

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14. A triode has  $r_p=10\Omega$  and  $\mu=20$ . Table

gives the operating point with blanks.



The values of  $E_{b1}$  and  $E_{c1}$  are

A. 290 V, -6V

B. 210 V, -10V

C. 290 V, -10V

D. 210 V, -6V

#### Answer: D





**15.** For a vacuum triode, the values of  $E_c$ ,  $E_b$ and  $l_b$  are given as under



The values of  $\mu$  and  $r_p$  are given by

A. 
$$\mu=12, r_p=10k\Omega$$

B. 
$$\mu=12, r_p=20k\Omega$$

C. 
$$\mu=10, r_p=20k\Omega$$

D. 
$$\mu=20, r_p=10k\Omega$$

#### Answer: D



**16.** The temperature (T) dependence of resistivity (rho) of a semiconductor is represented by :









#### Answer: C



**17.** In Fig.  $V_0$  is the potential barrier across a p-n junction, when no battery is connected across the junction :



A. 1 and 3 both correspond to forward bias

of junction

- B.3 corresponds to forward bias of junction and 1 corresponds to reverse bias of junction
- C.1 corresponds to forward bias and 3

corresponds to reverse bias of junction.

D. 3 and 1 both correspond to reverse bias of junction.

Answer: B



**18.** In BJT, maximum current flows in which of the following?

A. Emitter region

B. Base region

C. Collector region

D. Equal in all the regions

#### Answer: A



**19.** A tungsten emitter works at 2500 K. To increase the emission current density by 20%, how much change in the work function is required (Given, log2=0.3, log 3= 0.477)

A. 0.016 eV

B. 0.039 eV

C. 2.54 eV

D. 0.254 eV

#### Answer: B



**20.** In an unbiased p-n junction electrons diffuse from n-region to p-region because :-

A. free electrons in the n-region attract them

B. they move across the junction by the potential difference

C. hole concentration in p-region is more

as compared to n-region.

D. All of the above

Answer: C

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**21.** Zener breakdown in a semi-conductor diode occurs when

A. forward current exceeds certain value

B. reverse bias exceeds certain value

#### C. forward bias exceeds certain values.

D. potential barrier is reduced to zero

Answer: B

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22. The voltage gain of an amplifier state is

100. The gain expressed in db is

A. 100

B. 20

C. 40

D. 10

#### Answer: C

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# **23.** If A = B = 1, then in terms of Boolean algebra the value of A. B + A is not equal to.

A. B.A+B

B. B+A

С. В

D. None

Answer: D

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### 24. The Boolean equation for the circuit given

in figure is



A. Y = 
$$\overline{A}$$
.B+C  
B. Y= $\overline{A}$ . $)\overline{B} + \overline{C})$   
C. Y =  $\overline{A}$ . $(B + \overline{C})$   
D. Y =  $\overline{A}$ . $(B + C)$ 

#### Answer: D



**25.** In the above circuit, if the polarity is reversed of battery, the current flowing would

A. 0 mA

B. 2 mA

C. 5 mA

D. 10 mA

Answer: A



**26.** The given figure shows a rectifier of alternating current (f=50 c/s)`, the number of

pulses of rectified current obtained in 1s is



A. 50

B. 25

C. 100

D. 200

Answer: C



**27.** In sample of pure silicon  $10^{13}$  atom  $/cm^3$  is mixed of phosphorus. If all doner atoms are active then what will be resistivity at  $20^\circ C$  if mobility of electron is  $1200cm^2$  / Volt sec :-

A. 0.5209  $\Omega$ -cm

B.  $5.209\Omega$ -cm

C.  $52.09\Omega$ -cm

D.  $520.9\Omega$ -cm

Answer: D



**28.** For the given combination of gates, if the logic states of inputs A, B, C, are as follows A = B = C = 0 and A = B = 1, C = 0 then the logic states of output D are



#### A. 0,0

#### B. 0,1

#### C. 1,0

D. 1,1

#### Answer: D

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**29.** The plate voltage of a triode is increased from 225 V to 250 V, the grid voltage is changed from 4 to -4.7V to maintain plate current constant. The amplification factor of the tube is

B. 2

C. 70

D. 20

Answer: A

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**30.** In a triode amplifier, the load resistance is equal to the plate resistance  $r_p$ . If  $\mu$  is the amplification factor, the stage gain of the amplifier is
A.  $\frac{\mu}{2}$ 

B.  $\mu$ 

 $\mathsf{C.}\,2\mu$ 

D. 
$$\frac{\mu}{4}$$

#### Answer: A



**31.** The triodes P and Q have the same amplification factor 40. Their plate resistances are  $4k\Omega$  and  $8k\Omega$ , respectively. If an amplifier

circuit is designed using anyone of them and a load resistance is of  $8k\Omega$ , the ratio of the voltage gain obtained from them will be

- A. 2:3
- B.4:3
- C.3:1
- D. 1:2

#### Answer: B

**32.** When the plate voltage of a triode is 150 V, its cut-ff voltage is -5V. On increasing the plate voltate to 200V, the cut-off voltage can be

A. - 4.5V

- $\mathrm{B.}-5.0\mathrm{V}$
- C. 2.3 V
- $\mathsf{D.}-6.66V$

#### Answer: D



**33.** In a diode vaccum tube, the plate currentis 5 mA, when the plate voltage is 160 V, a grid is introduced between the plate and cathode and a voltage of -2V is applied to it. The plate current will becomes

(if  $g_m=5 imes 10^{-3}\Omega^{-1}$ 

A. 20 mA

B. 10 mA

C. 4 mA

D. 7.5 mA

# Answer: C Watch Video Solution **34.** In common emitter amplifier, the $\frac{l_c}{l_e}$ is 0.98, then the value of $\beta$ is A. 98 B. 0.98

C. 49

D. None of these





# **35.** For the given circuit of p-n junction diode, which of the following statement is correct?

# A. In forward biasing the voltage across R

is V

B. In forward biasing the voltage acros R is

C. In reverse biasing the voltage acorss R is

V

D. In reverse biasing the voltage across R is

2V

#### Answer: A



**36.** When forward bias is applied to a P-N

junction, then what happence to the potential

barrier  $V_B$ , and the width of charge depleted region x?

- A.  $V_B$  increases, x decreases
- B.  $V_B$  increases, x increases
- C.  $V_B$  increases, x increases
- D.  $V_B$  decreases, x decreases

Answer: D

**37.** Carbon, silicon and germanium atoms have four valence electrons each. Their valence and conduction bands are separated by energy band gaps represented by  $(E_g)_C$ ,  $(E_g)_{Si}$  and  $(E_g)_{Ge}$ , respectively. Which one of the following relationship is true in their case?

A.  $\left(E_g
ight)_c>\left(E_g
ight)_{si}$ 

$$\mathsf{B.}\left(E_g\right)_C = \left(E_g\right)_{Si}$$

 $\mathsf{C.}\left(E_g\right)_C < \left(E_g\right)_{Ge}$ 

D.  $\left(E_g\right)_C < \left(E_g\right)_{Si}$ 

#### Answer: A



**38.** What is the plate current in a diode valve under the space charge limited operation, when the plate potential is 60 V? In a diode valve, the plate current is 320 mA, then the plate potential is 240 V.

A. 30 mA

B. 20 mA

C. 40 mA

D. 10 mA

#### Answer: C



**39.** In the following common emitter configuration, and n-p-n transistor with current gain  $\beta = 100$  is used. The output voltage of the amplifier will be



A. 10 mV

B. 0.1 V

C. 1.0 V

D. 10 V

Answer: C

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**40.** In the case of forward biasing of p-n junction, which one of the following figures

correctly depicts the directioin of the flow of

charge carriers?







#### D. None of these

#### Answer: C



**41.** In a forward biased p-n junction diode, the potential barrier in the depletion region will be of the form







#### Answer: D



42. The input resistance of a common emitter transistor amplifier, if the output resistance is  $500k\Omega$ , the current gain lpha=0.98 and the power gain is  $6.0625 imes 10^6$  is

A.  $198\Omega$ 

 $\mathsf{B.}\,300\Omega$ 

 $\mathsf{C}.\,100\Omega$ 

D.  $400\Omega$ 

Answer: A

**43.** In a figure given alongside assuming the diodes to be ideal



A.  $D_1$  is forward biased and  $D_2$  is reverse biased and hence, current flows from A to B

B.  $D_2$  is forward biased and  $D_1$  is reverse biased and hence, no current flows from

B to A and vice-versa

C.  $D_1$  and  $D_2$  are both forward biased and

hence current flows from A to B

D.  $D_1$  and  $D_2$  are both reverse biased and

hence no current flows from A to B and

vice-versa

Answer: B

**44.** In the circuit shown in figure, if the diode forward voltage drop is 0.3 V, then the voltage difference between A to B is,



A. 1.3 V

B. 2.3 V

C. zero

D. 0.5V

Answer: B





**45.** the exclusive -OR of A,B is represented by

 $A \oplus B$ . An equivalent form is

A.  $AB+\overline{A}\overline{B}$ 

 $\mathsf{B}.\,AB + \overline{A+B}$ 

 $\mathsf{C}.\,A\overline{B}+\overline{A}B$ 

D. None of these

Answer: C

# **46.** Given the truth table relating Y to A, B.

The outpur Y is

A. A+B

B. AB

 $\mathsf{C}.\,\overline{A}B$ 

 $\mathsf{D}.\,\overline{A+B}$ 

#### Answer: D

# **47.** Given the followig truth table, where A,B are inputs and Y the output



A.  $A\overline{B}$ 

 $\mathsf{B}.\,\overline{A}B$ 

C. AB

D.  $\overline{A}B$ 

#### Answer: D





**48.** What will be the input A and B for the Boolean expression  $(\overline{A + B}) \cdot (\overline{A \cdot B})$ =1?

A. 0,0

B. 0,1

C. 1,0

D. 1,1

Answer: A



**49.** Which of the following gates will have an output of 1?





#### Answer: C



# 50. Current in the circuit will be



# A. 5/40

- B. 5/50
- C. 5/10
- D. 5/20

#### Answer: B

# **51.** Find the value of $V_{AB}$ .



#### A. 10 V

- B. 20 V
- C. 30 V
- D. None of these

#### Answer: A



**52.** A two volt battery forward biased and a diode. However, there is a drop of 0.5 V across the diode which is independent of current. Also, a current greater than 10 mA produces large joule loss and damages diode. If diode is to be operated at 5 mA, then the series resistance to be put is

A. 3 k $\Omega$ 

B. 300 k $\Omega$ 

 $\mathsf{C}.\,300\Omega$ 

## D. 200 k $\Omega$

#### Answer: C

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**53.** Which of the following statements concerning the depletion zone of an unbiased p-n junction is (are) true?

A. The width of the zone is independent of

the densities of the dopants (impurities).

B. The widthof the zone is dependent on

the densities of the dopants

C. The electric field in the zone is produced

by ionised dopant atoms

D. The electric field in the zone is provided

by electrons in the conduction band and

the holes in the valence band

Answer: B

54. The circuit shown in figure below will act as



A. OR gate

B. AND gate

C. XOR gate

D. None of these

Answer: A

## 55. The circuit shown below will act as



A. AND gate

B. OR gate

C. NAND gate

D. XOR gate

Answer: A



56. In Boolean algebra, A + B = Y implies that

A. sum of A and B is Y

B. Y exists when A exists or B exists or both

A and B exist

C. Y exists only when A and B both exist

D. Y exist when A or B exist but not when

both A and B exist

Answer: D

**57.** In the Boolean algebra, the following one is wrong

- A. 1+0=1
- B. 0+1=1
- C.1 + 1 = 1
- D. None of these

#### Answer: A

58. The output of a 2-input OR gate is fed to a

NOT gate, the new gate obtained is

A. OR gate

B. NOT gate

C. NOR gate

D. XOR gate

Answer: C

59. Digital circuit can be made by the

repetitive use of

A. OR gate

B. AND gate

C. NOT gate

D. NAND gate

Answer: D

1. The circuit is equivalent to



A. AND gate

B. OR gate

C. NOT gate

D. None of these

**Answer:** 

**2.** Active state of n-p-n transistor, in circuit is achieved by

A. low input voltage

B. high input voltage

C. both a and b

D. Neither a nor b

#### Answer: D
3. If a semiconductor has an intrinsic carrier concentration of  $1.41 imes 10^{16}\,/\,m^3$  when doped with  $10^{21}\,/\,m^3$  at room temperature will be

A.  $2 imes 10^{21}$ 

 $\mathrm{B.2}\times10^{11}$ 

 $\mathsf{C.}\,1.41\times10^{10}$ 

D.  $1.41 imes 10^{16}$ 

Answer: D

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**4.** Two identical capacitors eachof capacitance C are charged to the same potential V and are connected in two circuits (i) and (ii) at t=0 as shown. The charges on the capacitor at t=CR

are

A. 
$$\frac{CV}{e}, \frac{CV}{e}$$

B. CV, CV

C. 
$$\frac{VC}{e}$$
,  $VC$   
D.  $VC$ ,  $\frac{VC}{e}$ 

## Answer: C



**5.** A transistor is preferable to a triode valve when used in amplifier because it

- i) Can withstand large changes in tempratures
- ii) has a higher input impedance
- iii)can handle larger powers
- (iv) does not require powers
- Which of the above statements is correct?

A. Only (i), (ii), and (iii) are correct

B. Only (i), and (iii) are correct

C. Only (ii) and (iv) are correct

D. Only (vi) is correct.

Answer: D

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