PHYSICS

BOOKS - BETTER CHOICE PUBLICATION

DIGITAL ELECTRONICS

Very Short Answer Type Questions

1. Write the truth table of OR gate.

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2. Write the truth table of AND gate.

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3. Write the truth table of 'NOT' logic gate .

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4. Write down the truth table of AND gate and also draw its logic symbol.

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5. Write down the truth table of OR gate and also draw the logic symbol.

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6. What is logic gate ?

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7. Draw the logic symbol of AND gate.Write its truthtable also. How can one realise such a gate in actual practice by using junctions diodes ?

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8. Draw the logic symbol of NOR gate.

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9. Draw the logic symbol of NAND gate.

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10. The following truth table represents .

A	B	y
0	0	1
1	0	0
0	1	0
1	1	0

A. AND gate

B. NOR gate

C. OR gate

D. NAND gate .

Answer: B

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11. The following truth table represents :

A	B	y
0	0	1
1	0	1
0	1	1
1	1	0

A. AND gate

B. NOR gate

C. OR gate

D. NAND gate .

Answer: D

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12. The output of OR gate is 1

A. if either input is zero

B. if both inputs are zero

C. only, if both inputs are 1

D. if either or both inputs are 1 .

Answer: d

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Short Answer Type Questions

1. Draw the logic symbol for 'OR' logic gate and write its truth tale .

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2. Draw the logic symbol for 'AND' logic gate and write its truth table .

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3. Draw the logic symbol for 'NOT' logic gate and write its truth table .

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4. Draw the logic symbol and write truth table of NAND logic gate

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5. Draw the logic symbol and write truth table of NOR logic gate

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6. How 'NAND'logic gate is obtained from basic gates ? Draw its logic symbol and write its truth table.

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7. How 'NAND'logic gate is obtained from basic gates ? Draw its logic symbol and write its truth table.

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8. Give the logic symbol, truth table andBoolean expression for NOR gate.

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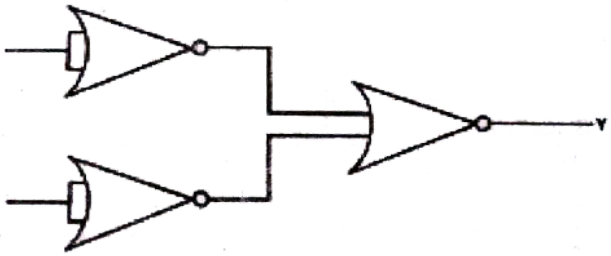
9. Make the truth table for all possible inputs of the following circuit .



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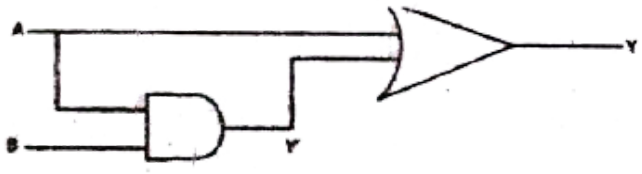
10. Make the truth table for all possible inputs of the following gates .



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11. What is logic symbol and truth table for NAND gate ? Also give its Boolean expression.

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12. Write logic symbol, Boolean expression.and truth table of AND and NOR gate.

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13. How would you set up a circuit to obtain NOT gate using a transistor?

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14. Write the truth table of NAND gate.Draw its symbol. Draw a circuit to obtain NOT gate using NAND gate only.

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15. Draw a circuit to obtain OR gate using NAND gate only .

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16. Write Boolean expression and the truth table of OR

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17. What is logic symbol and truth table for NAND gate ? Also give its Boolean expression.

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18. Give the logic symbol, truth table and Boolean expression for OR gate.

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19. Give the logic symbol, truth table andBoolean expression for NOR gate.

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20. Give the logic symbol, truth table andBoolean expression for AND gate.

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21. Draw the logic symbol of NAND gate.

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22. Write Boolean expression and the truth table of OR

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23. Write logic symbol, Boolean expression and truth table of NAND and OR gate.

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24. Write logic symbol, Boolean expression and truth table of NOT and OR gate.

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Long Answers Type Questions

1. Draw the logic symbol of NOT gate. Write. its truth table. Explain, how this gate can be realized in actual practice by using transistor

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2. Draw the logic symbol of OR gate. Write its truth table. Explain how this gate can be realized in actual practice by using junction diodes.

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3. Draw the logic symbol of OR gate. Write its truth table. Explain how this gate can be realized in actual practice by using junction diodes.

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